

A2
concl'd
sub
B1
opp.

4. (Amended) A card with a microprocessor and contacts according to Claim 2 [or 3, characterised in that] wherein the checking circuit [(36) comprises means to check] checks for the presence [or not] of a binary parity digit or a cyclic redundancy code and [to supply] supplies a corresponding signal or code.

Add the following new claim:

A3


--5. A card with a microprocessor and contacts card according to Claim 3, wherein the checking circuit checks for the presence of a binary parity digit or a cyclic redundancy code and supplies a corresponding signal or code.--

REMARKS

Entry of the foregoing amendments is respectfully requested. These amendments are intended to further clarify the language of the claims and specification. It is to be noted that the amendments have been made with respect to the claims as they appear in the annex to the International Preliminary Examination Report.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

By: 
James A. LaBarre
Registration No. 28,632

P.O. Box 1404
Alexandria, Virginia 22313-1404
(703) 836-6620

Date: July 27, 2000